Changes in the Service Packs

This page provides an overview of the most important changes in the Service Packs of **CST STUDIO SUITE 2017**.

Contents

Service Pack 2017.01

See also

What's New in this Version
Comprehensive Feature List

Service Pack 2017.01

General / Environment

- Corrected icon size in CST Update Manager when the system font is too large.
- Repaired encoding problem in Ribbon combo boxes.
- Fixed issue with enabled state of Ribbon split buttons.

System Assembly Modeling

- Corrected block updates in the layout which contain lumped elements.
- CHT solver settings are now taken into account for simulation project reference model inheritance.

EDA Import / EDA Tools

- Fixed issue with changed port impedance when opening a CST project from a previous version.

Distributed Computing (DC)

- Fixed multiple writing of header in log file after remote connection was established.

CST MICROWAVE STUDIO - Transient Solver

- Fixed setup error in case of Hyper-threading and more than one Hardware Acceleration device.

CST MICROWAVE STUDIO - Transient Solver (TLM Mesh)

- Latitude settings for TLM solver are now correctly restored.

CST MICROWAVE STUDIO - Frequency Domain Solver (Tetrahedral Mesh)

- Improved stability of Frequency distributed computing with farfield probes.
- The second Floquet port mode with the option *polarization independent of the scan angle phi* is handled correctly.

CST MICROWAVE STUDIO - Asymptotic Solver

- Improved stability when using near field source together with field monitors.
- Fixed wrong hotspot scaling when using SBR Raytubes.

CST EM STUDIO - General

- Repaired the time limits accuracy problem for the iron loss monitor
- Improved mesh invalidation during post processing solver runs after distributed computing simulations.

CST EM STUDIO - LF Frequency Domain Solver (Tetrahedral Mesh)

- Improved stability of impedance calculation in LF FD TET solver.
- Fixed calculation of an admittance matrix in the EQS LF TET solver.

CST PCB STUDIO

- PI Solver: Repaired pin grouping when components are switched off.

CST DESIGN STUDIO

- Improved CST file creation after calculating a CST PCBS file block from CST DS in hidden mode.
- Repaired deletion of electrical machine tasks.
- Improved opening of CST CS projects in case of a failed rebuild.
- Allow corner changes in the IBIS dialog box without pressing Edit.
- Save project after conversion of legacy IBIS blocks and delete relict IBIS files.
- Prevent relative file path irregularity from impeding the conversion of legacy IBIS blocks.
- Proceed with conversion of legacy IBIS blocks despite errors.
- Re-enabled selection of heterogeneous differential IBIS buffers.

Interference Tool

- Corrected point margin and secondary received power density calculation for spectra with Dirac-Delta tones.
- Improved violation matrix tooltip.

See also

What's New in this Version, Comprehensive Feature List, Upgrade Information